

FORM PTO-1449  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <i>(Use several sheets if necessary)</i>	SEP 16 2005 U.S. PATENT AND TRADEMARK OFFICE	Docket Number (Optional) 81839.0107	Application Number 10/009,910
		Applicant IIDA, et al.	
		Filing Date December 12, 2001	Group Art Unit 1722

## **U.S. PATENT DOCUMENTS**

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
MS	2001-274166	10/05/01	JAPAN			Abstract	

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

MS	"MOS Device Epitaxial Wafer", edited by Hirota Sumitomo, Realize Limited 6/30/98, pgs. 13-15

<b>EXAMINER</b> /Matthew Song/	<b>DATE CONSIDERED</b> 10/03/2006
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**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

**INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION**

**MAY 01 2006 (Use several sheets if necessary)**

**Filing Date**

IIDA, et al.

**Filing Date**  
December 12, 2001

**Group Art Unit**

## **U.S. PATENT DOCUMENTS**

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
MS	08-330316	12/13/96	JAPAN			Abstract	

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

<b>EXAMINER</b> /Matthew Song/	<b>DATE CONSIDERED</b> 10/03/2006
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